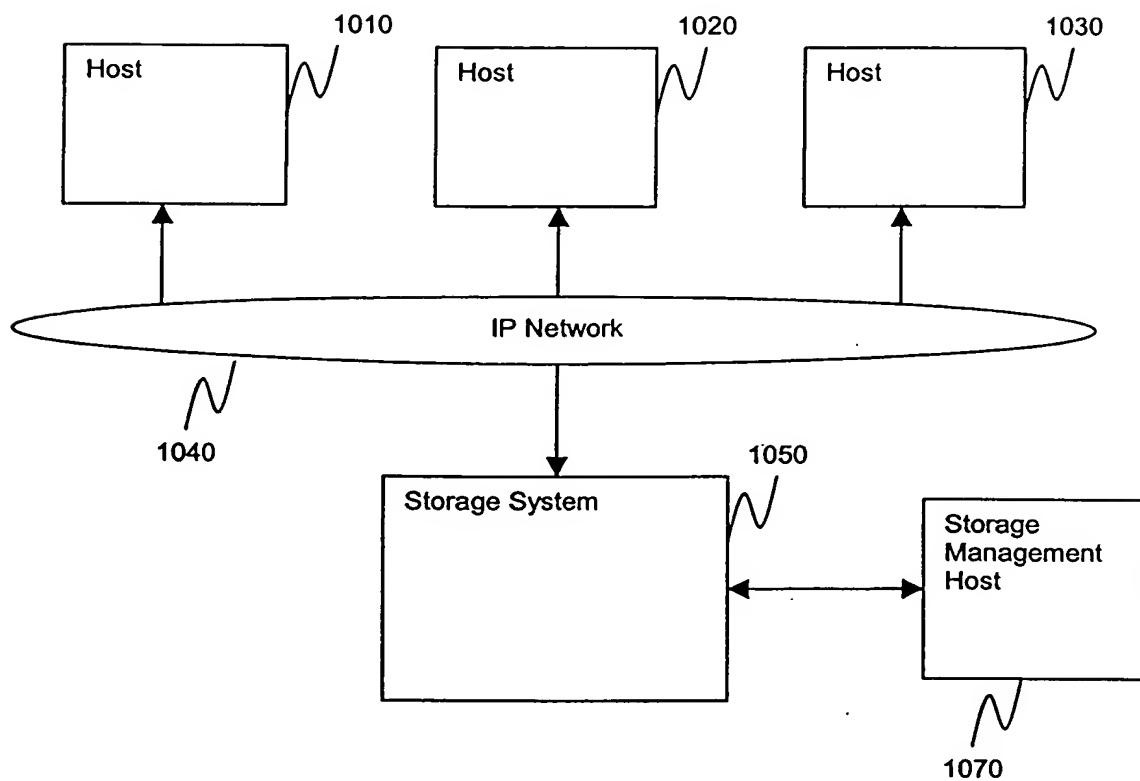


**Fig 1: System configuration of using storage system.**



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Fig 2: Storage System

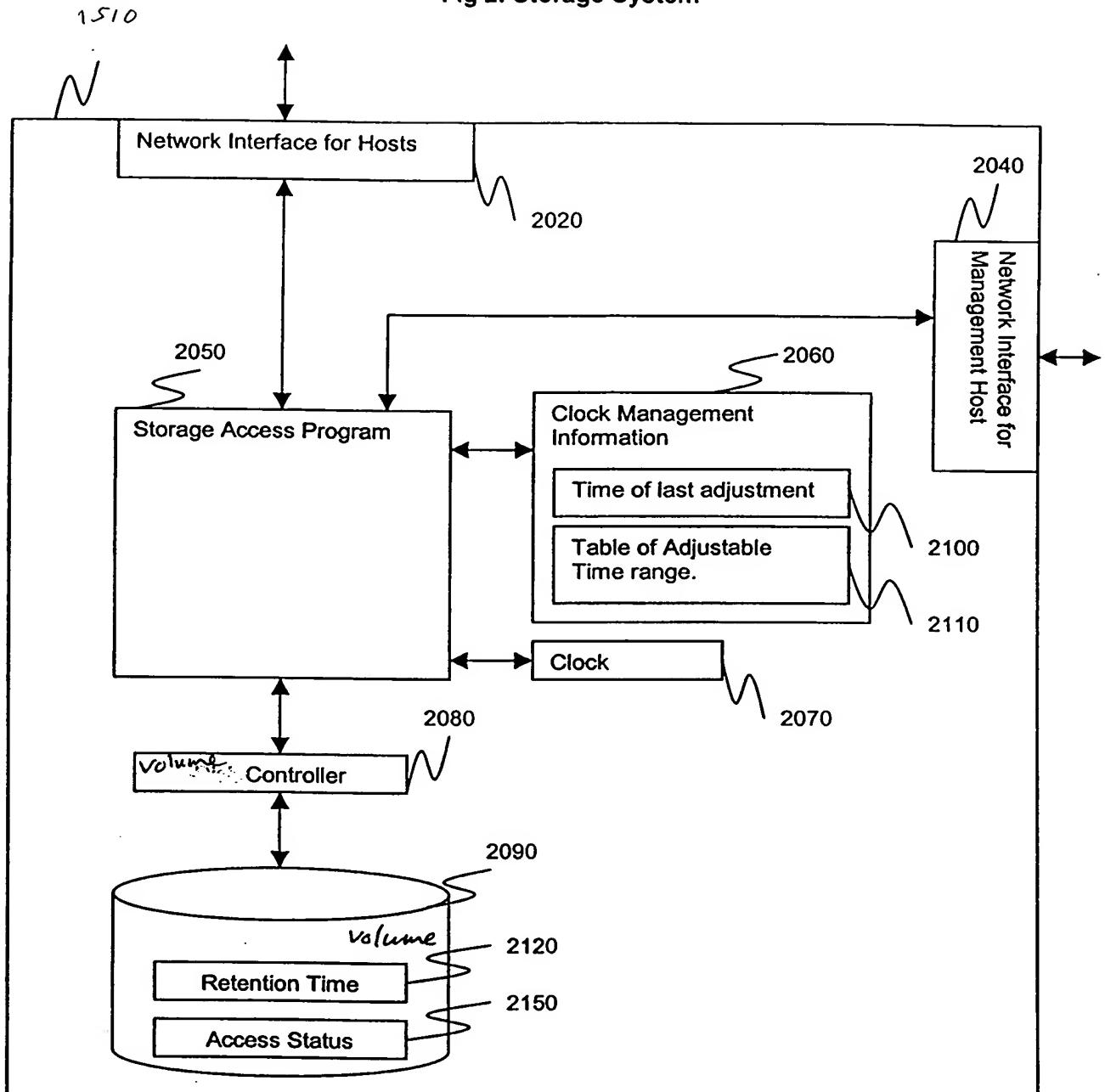


Fig 3: Table of Adjustable Time Range

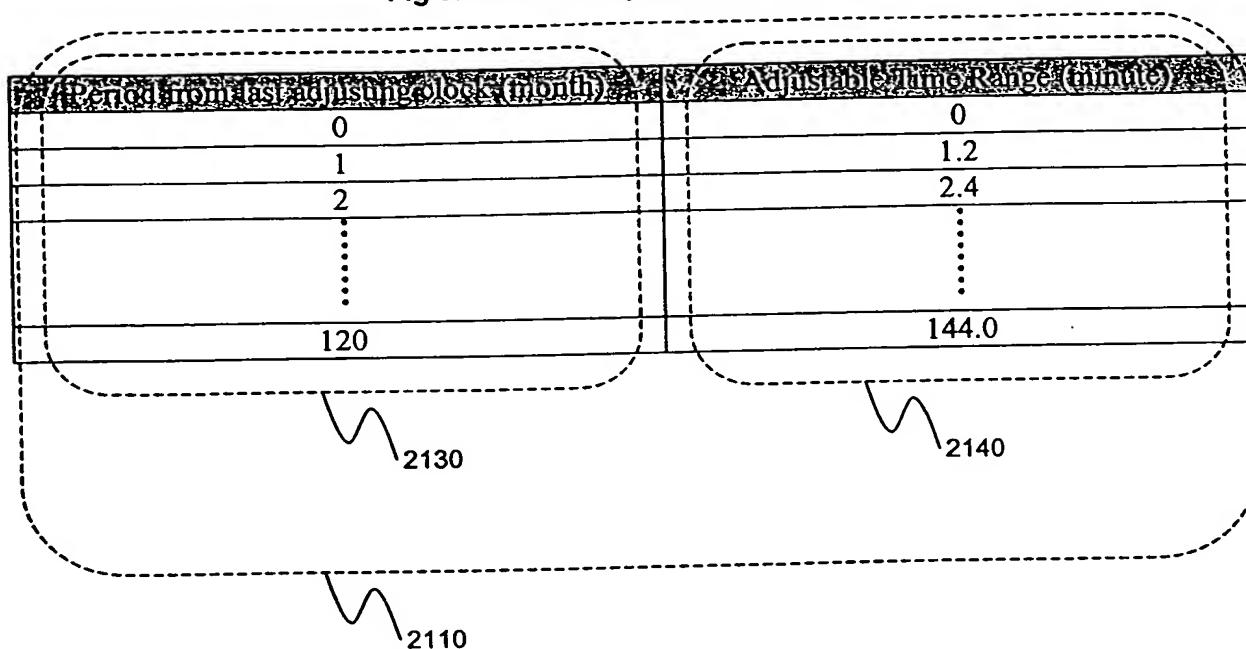


Fig 4: Time distribution from accidental error.

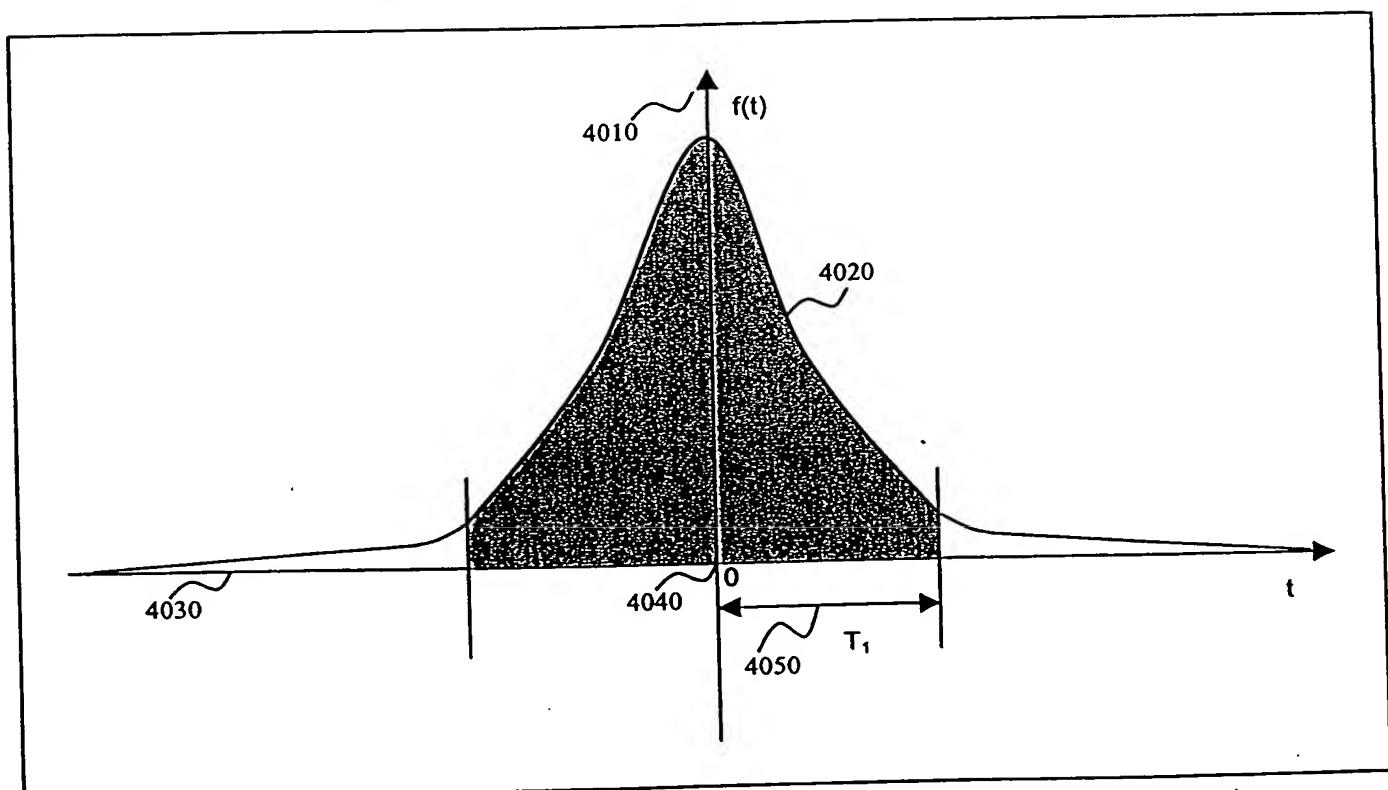
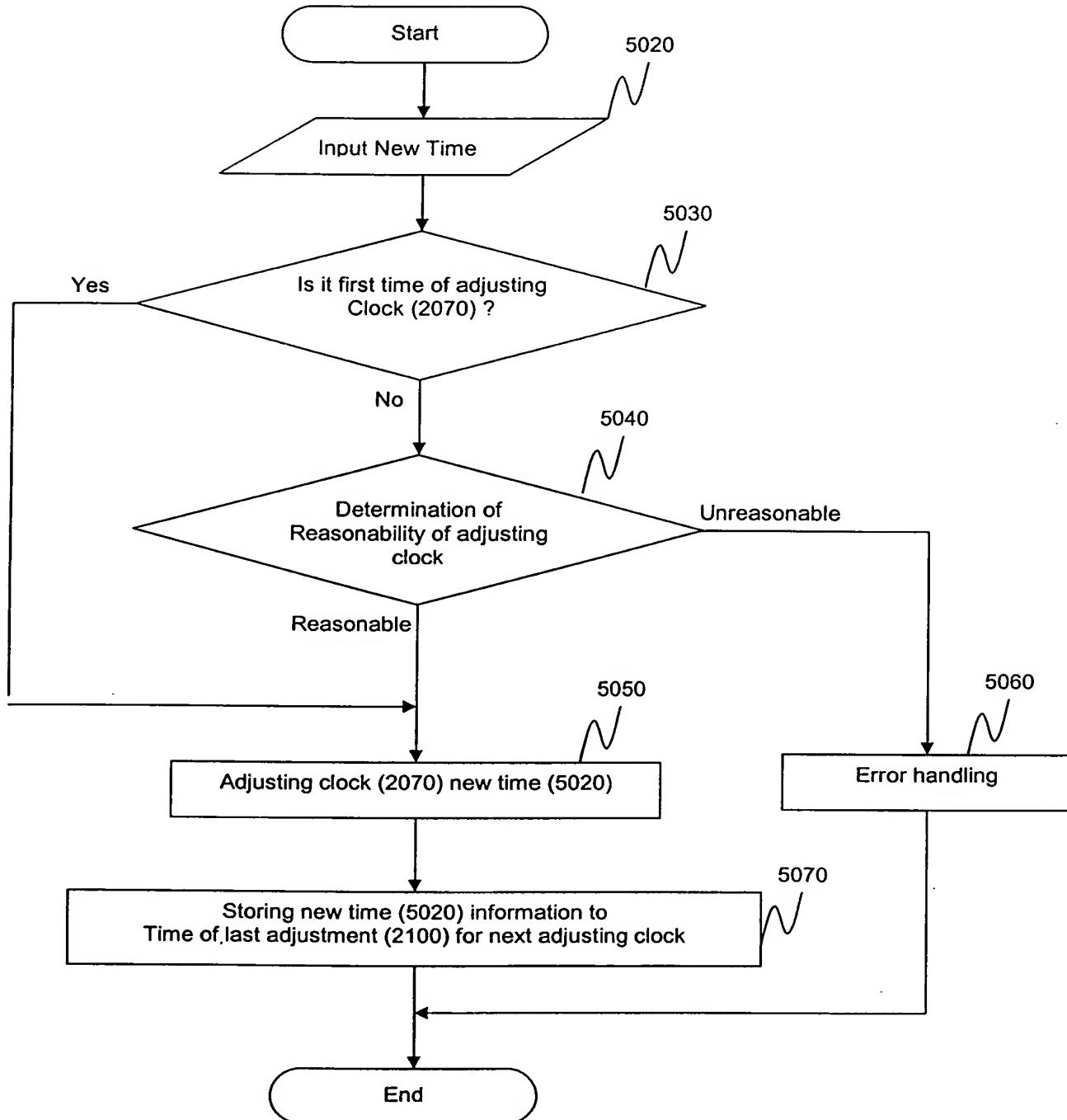


Fig 5: Adjusting Clock Flow for Storage System



**Fig 6: Determination of Reasonability of adjusting clock**

